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CONFIRMATION NO. 9192

SERIAL NUMBER 10/519,745	FILING OR 371(c) DATE 12/29/2004 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. Q85443
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**APPLICANTS**

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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/JP03/08786 07/10/2003

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature _____ Initials _____				

**ADDRESS**

23373

**TITLE**

Method of protecting semiconductor device and protection apparatus for semiconductor device using the same

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